

ModelSim

Website

eda.sw.siemens.com/en-US/ic/modelsim

Summary

ModelSim is a multi-language environment by Mentor Graphics, for the simulation of hardware description languages such as VHDL, Verilog, and SystemC, and includes a built-in C debugger.

Who can use this service?

Garwood Hall Lab(s)

Internal Data (Requires Login)

Status: Acquired

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